



S/N 09/607,421

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Thomas A. Tetzlaff	Examiner:	Samuel Broda
Serial No.:	09/607,421	Group Art Unit:	2123
Filed:	June 30, 2000	Docket No:	884.279US1
Title	DIGITAL CIRCUIT SIMULATION		
Assignee:	Intel Corporation	Customer No:	21186

PETITION FOR A ONE-MONTH EXTENSION OF TIME

MS RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Final Rejection mailed November 2, 2004, said period of response being extended from February 4, 2005 to March 4, 2005.

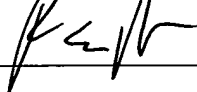
**Our check in the amount of \$120.00 is enclosed to cover the required extension fee. Please charge any additional fees or credit overpayment to deposit Account No. 19-0743.**

Respectfully Submitted

THOMAS A. TETZLAFF

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Date: 2 March 2005 By:   
Robert E. Mates  
Reg. No: 35,271

**CERTIFICATE UNDER 37 CFR § 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 2nd day of March 2005.

Name: Amy Moriarty Signature: 